

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A memory module of an ~~optionally portable electronic device having a processor which optionally provides an overall operation control of said electronic device~~, comprising:

a fast non-volatile random access memory, responsive to a command/data signal provided by ~~said a processor~~, for providing a permanent storage of information before said command/data signal is provided, for executing a command ~~containedcomprised~~ in said command/data signal using said permanently stored information ~~thus for~~ providing a direct communication between said fast non-volatile random access memory and the processor of ~~said optionally portable electronic device~~.

2. (Currently Amended) The memory module of claim 1, wherein an interface between the processor and the fast non-volatile random access memory is a double data rate (DDR) type.

3. (Currently Amended) The memory module of claim 1, wherein the fast non-volatile random access memory provides a temporal storage of data ~~containedcomprised~~ in said command/data signal.

4. (Original) The memory module of claim 3, wherein said fast non-volatile random access memory comprises:

an information storage area for the permanent storage of said information; and a temporal data storage area for the temporal storage of said data.

5. (Original) The memory module of claim 4, wherein said fast non-volatile random access memory further comprises:

at least one register for setting operating parameters of the fast non-volatile random access memory or protecting said data or said information during said execution.

6. (Currently Amended) The memory module of claim 5, wherein said operating parameters ~~contain~~comprise one or more of: a) timings for a particular frequency, ~~or~~and b) frequency ranges with a corresponding core voltage range, ~~or~~both said timings and said frequency ranges.

7. (Currently Amended) The memory module of claim 5, wherein said protecting ~~contains~~comprises a write protection.

8. (Currently Amended) The memory module of claim 1, wherein said information ~~contains~~comprises an application program for operating an electronic device.

9. (Original) The memory module of claim 1, further comprising:  
a mass memory, for providing further information in response to a command/information signal; and  
an application-specific integration circuit, responsive to said command/data signal, for providing said command/information signal.

10. (Original) The memory module of claim 9, wherein said further information is provided to said fast non-volatile random access memory.

11. (Currently Amended) The memory module of claim 10, wherein said fast non-volatile random access memory executes a further command ~~contained~~comprised in said command/data signal using said further information.

12. (Currently Amended) The memory module of claim 9, wherein an interface between the application-specific integration circuit and the fast non-volatile random access memory is a double data rate (DDR)-type.

13. (Currently Amended) The memory module of claim 9, wherein a non-volatile random access memory-integrated circuit (~~NVRAM-IC~~) package ~~contains~~comprises the application-specific integration circuit, the mass memory and the fast non-volatile random access memory, or said non-volatile random access memory-integrated circuit (~~NVRAM-IC~~)

package ~~containscomprises~~ the application-specific integration circuit and the fast non-volatile random access memory, or said non-volatile random access memory-integrated circuit (~~NVRAM IC~~) package ~~containscomprises~~ the mass memory and the fast non-volatile random access memory.

14. (Original) The memory module of claim 9, further comprising:  
a dynamic random access memory, responsive to a command/data signal, for providing a storage of said further information, wherein said further information is provided or partially provided to the dynamic random access memory by the mass memory in response to said command/information signal.

15. (Currently Amended) The memory module of claim 14, wherein a non-volatile random access memory-integrated circuit (~~NVRAM IC~~) package ~~containscomprises~~ the application-specific integration circuit, the mass memory, the fast non-volatile random access memory and the dynamic random access memory, or said non-volatile random access memory-integrated circuit (~~NVRAM IC~~) package ~~containscomprises~~ the application-specific integration circuit and the fast non-volatile random access memory, or said non-volatile random access memory-integrated circuit (~~NVRAM IC~~) package ~~containscomprises~~ the mass memory, the dynamic random access memory and the fast non-volatile random access memory.

16. (Currently Amended) The memory module of claim 14, wherein said dynamic random access memory executes a still further command ~~containedcomprised~~ in said command/data signal using said further information.

17. (Currently Amended) The memory module of claim 14, wherein ~~said portable an~~ electronic device comprises:

a removable mass memory, for providing, in response to a further command/information signal provided by the application-specific integration circuit, still further information to the fast non-volatile random access memory, or to the dynamic random access memory, or to both the fast non-volatile random access memory and to the dynamic random access memory.

18. (Currently Amended) The memory module of claim 17, wherein said fast non-volatile random access memory or the dynamic random access memory or both the fast non-volatile random access memory and the dynamic random access memory execute a further command or a still further command or both the further command and the still further command containedcomprised in said command/data signal using said further information or said still further information or both the further information and the still further information.

19. (Original) The memory module of claim 1, wherein said fast non-volatile random access memory is a magneto-resistive random access memory, a ferroelectric random access memory, or an Ovonics memory type.

20. (Currently Amended) An electronic device, comprising:  
a processor, for providing a command/data signal and optionally for providing an overall operation control of said electronic device; and  
a fast non-volatile random access memory, responsive to the command/data signal, for providing a permanent storage of information before said command/data signal is provided, for executing a command containedcomprised in said command/data signal using said stored information.

21. (Original) The electronic device of claim 20, further comprising:  
a power and reset block, for resetting said processor and for resetting said fast non-volatile random access memory.

22. (Original) The electronic device of claim 20, wherein said electronic device is a portable electronic device, a mobile electronic device or a mobile phone.

Claims 23-32 are cancelled

33. (New) An apparatus, comprising:

means for responding to a command/data signal provided by a processor, for providing a permanent storage of information before receiving said command/data signal, for executing a command comprised in said command/data signal using said permanently stored information to provide a direct communication between said means and the processor.

34. (New) The apparatus of claim 33, wherein said apparatus is a memory module.